

オープンソースソフトウェアのライセンス通知事項

ApresiaNP シリーズ

ApresiaLightFM/GM/GM152GT

Apresia (AEOS 8/AEOS 7)

APRESIA Systems 株式会社

制定・改訂来歴表

No.	年 月 日	内容
-	2014年12月16日	新規制定
		• 表紙 題名および体裁変更
		• はじめに、奥付の社名変更対応
Α	2020年8月7日	
		• はじめに 本文および表 1 記載変更
		• ライセンス通知事項の記載変更

はじめに

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Cryptographic attack detector for ssh - source code

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@version 3.0 (December 2000)

*

Optimised ANSI C code for the Rijndael cipher (now AES)

*

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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- 3. [3]Viraj Bais <vbais@mailman1.intel.com> and [4]Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
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- 24. [26]Frank Kardel [27]<kardel (at) ntp (dot) org> PARSE <GENERIC> driver (>14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
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- 26. [29]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
- 27. [30]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
- 28. [31]George Lindholm < lindholm@ucs.ubc.ca> SunOS 5.1 port
- 29. [32]Louis A. Mamakos < Iouie@ni.umd.edu> MD5-based authentication
- 30. [33]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
- 31. [34]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
- 32. [35]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics; audio clock drivers: CHU, WWV/H, IRIG
- 33. [36]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
- 34. [37]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
- 35. [38]Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port
- 36. [39]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
- 37. [40]Derek Mulcahy <derek@toybox.demon.co.uk> and [41]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
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- 39. [43]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
- 40. [44]Wilfredo S瘤 chez <wsanchez@apple.com> added support for NetInfo
- 41. [45]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
- 42. [46] Jack Sasportas < jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
- 43. [47]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
- 44. [48]Michael Shields <shields@tembel.org> USNO clock driver
- 45. [49]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
- 46. [50]Harlan Stenn harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
- 47. [51] Kenneth Stone < ken@sdd.hp.com> HP-UX port
- 48. [52]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
- 49. [53]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK clock driver
- 50. [54]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver

51. [55]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD

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11. Mathopd Project

This product includes Mathopd developed by Michiel Boland (http://www.mathopd.org/index.html).

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